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File 348:EUROPEAN PATENTS 1978-2003/Nov W05

(q) 2003 European Patent Office

File 349:PCT FULLTEXT 1979-2002/UB=20031203,UT=20031127

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Set	Items	Description
S1	1172145	EXTEN? OR UNDEFINED OR (NON OR "NOT" OR NO OR WITHOUT OR WITH() OUT OR OMIT? ? OR OMITT???) (3W) (DEFIN? OR DESIGNAT??? OR STIPULAT??? OR SET OR DETERMIN? OR ESTABLISH?) OR ADD??? OR A- DDITIONAL
S2	20347	MACRO? ? OR MACROINSTRUCTION? ?
S3	982988	COMMAND? ? OR INSTRUCTION? ? OR ELEMENT? ? OR ARGUMENT? ?
S4	1393846	RUN? ? OR RUNNING OR EXECUT??? OR PERFORM? OR ACTION? OR A- CTIVAT??? OR BEGIN? OR EFFECT??? OR ACTUAT??? OR LAUNCH OR IN- ITIAT???
S5	397	(NON OR "NOT" OR NO OR WITHOUT OR WITH() OUT OR OMIT? ? OR - OMITT???) (3N) (RECOMPIL??? OR REOPTIMIS??? OR REOPTIMIZ??? OR - RE() (COMPIL??? OR OPTIMIS??? OR OPTIMIZ???)
S6	7500	KEYWORD? ? OR KEY() (WORD? ? OR TEXT OR PHRASE? ?)
S7	335703	REGISTR??? OR REGISTER??? OR RECORD??? OR TABULAT???
S8	454486	ON(2W) (REQUEST OR DEMAND OR FLY) OR (AS OR WHEN) (2W) (NEEDED OR REQUIRED) OR JUST(2W) TIME OR DYNAMIC? OR JIT OR TO() ORDER OR REALTIME OR (REAL OR ACTUAL) () TIME OR BEHIND(2W) SCENE? ? OR ADAPTIV? OR TRANSPARENT?
S9	133	S2(10N) (S1(7N) S3)
S10	51	S4(5N) S5
S11	12	S6(5N) S7(5N) S8
S12	0	S9(S) S10(S) S11
S13	0	S2(S) (S10 OR S11)
S14	82	S4(10N) S5
S15	10	S8(10N) (S6(5N) S7)
S16	29	S6(10N) S7(10N) S8
S17	7	S2 AND (S14 OR S16)
S18	77	S2(5N) (S1(3N) S3)
S19	57240	IC=(G06F-017? OR G06F-007? OR G06F-009?)
S20	42	S18 AND S19
S21	44	S2-AND-((S4(S) S5) OR (S6(S) S7(S) S8))
S22	32	S19 AND S21
S23	32	IDPAT (sorted in duplicate/non-duplicate order)
S24	30	IDPAT (primary/non-duplicate records only)

24/3,K/2 (Item 2 m file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00952558

PROCESS CONTROL

PROZESSSTEUERVORRICHTUNG

COMMANDE DE PROCEDE

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PATENT (CC, No, Kind, Date): EP 937286 A1 990825 (Basic)

EP 937286 B1 020327

WO 9820418 980514

APPLICATION (CC, No, Date): EP 96935171 961105; WO 96GB2703 961105

DESIGNATED STATES: DE; FI; FR; GB; IE; IT; NL; SE

INTERNATIONAL PATENT CLASS: *G06F-009/46*

NOTE:

No A-document published by EPO

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS.B	(English)	200213	1108
CLAIMS B	(German)	200213	974
CLAIMS B	(French)	200213	1316
SPEC B	(English)	200213	17766

Total word count - document A 0

Total word count - document B 21164

Total word count - documents A + B 21164

INTERNATIONAL PATENT CLASS: *G06F-009/46*

...SPECIFICATION in a specialised, interpreted language.

Similarly, there is a repertoire of plug and socket types, *performing*
the various kinds of inter-cellular relationship and communication. These
make up an internal' construction...

...exist in soft-codable form, so that new plug and socket types can be
defined *without* needing to *re*-compile* the program. Hard- or
soft-coded cell types can be constructed from the various classes...very
important, as it allows new kinds of Gaia object to be imported into
simulations *without* *recompiling* them, and even *without* stopping
program *execution* (for example, by downloading them transparently
across the Internet).

Not only can scripts be stored...and compiling it into the cell
library, or by defining behaviour at run-time using *macro* scripts.

2. Apply polygon meshes made from appropriate materials in order to
render these cells...

24/3,K/6 (Item 6 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00431289

Incremental compiler for source code development system.

Inkrementeller Compiler fur ein Quellkodeentwicklungssystem.

Compilateur incrementiel pour un systeme de developpement de code source.

PATENT ASSIGNEE:

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PATENT (CC, No, Kind, Date): EP 406028 A2 910102 (Basic)
EP 406028 A3 930107

APPLICATION (CC, No, Date): EP 90307228 900702;

PRIORITY (CC, No, Date): US 375397 890630; US 375398 890630; US 375399
890630; US 375383 890630; US 375384 890630; US 375401 890630; US 375402
890630

DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FR; GB; GR; IT; LI; LU; NL; SE

INTERNATIONAL PATENT CLASS: *G06F-009/46*; *G06F-009/44*

ABSTRACT WORD COUNT: 348

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	5467
SPEC A	(English)	EPABF1	14271
Total word count - document A			19738
Total word count - document B			0
Total word count - documents A + B			19738

INTERNATIONAL PATENT CLASS: *G06F-009/46*...

...*G06F-009/44*

...ABSTRACT A2

A computer-aided software development system includes programs to
implement edit, compile, link and *run* sequences, all from memory, at
very high speed. The compiler and linker operate on an...

...in an edit session, then only that line and lines related to it need be
recompiled if *no* other code is affected; linking is also done in a
manner to save and re...

...lists which are not changed from one edit session to another. Dependency
analysis may be *performed* incrementally, without requiring the user to
enter dependencies. Scanning is also done incrementally, and the...

...Also, the object code created is maintained in memory rather than in a
file, and *executed* from this memory image, to reduce delays. A virtual
memory management arrangement for the system...

...SPECIFICATION modules 12 which have not been changed or are not
dependent upon changed code are *not* *recompiled*. If an error is found
by the compiler 11, the operation is returned by path...

...tables 14 plus a link table produced by the linker 15 along with
information from *run*-time libraries, but in any event the code image
(in memory) is *executed* as indicated by the *run* function 18. If logic
errors or runtime errors are discovered during the *run* phase, the
programmer returns to the edit phase. The code image 16, after being
run with no error reported, would be saved as debugged object code in
traditional systems; however...starting situation for later reuse.

Other journals are needed for some languages. Incremental preprocessing
for *macro* expansion is one example.

The saved information, mostly in journals, is attached to a context...
structure from the language. For some languages this is a trivial
mapping; for others with *macros* or keyword features, it may require a

computation and other tables.

Figure 8 shows the...However, up to this point, the compiler can skip over the clean or unchanged increment *without* *recompiling* code unnecessarily. The clean increment does not need to be parsed because the information that...

24/3,K/23... (Item 23 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00560546 **Image available**

METHOD AND SYSTEM FOR AN EXTENSIBLE *MACRO* LANGUAGE
PROCEDE ET SYSTEME POUR *MACRO*-LANGAGE EXTENSIBLE

Patent Applicant/Assignee:

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Inventor(s):

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MINTZ Eric,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200023919 A1 20000427 (WO 0023919)

Application: WO 99US24115 19991015 (PCT/WO US9924115)

Priority Application: US 98104682 19981016

Designated States: AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES

FI GB GE GH GM HU ID IL IS JP KE KG KR KZ LK LR LS LT LU LV MD MG MK MN

MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG US UZ VN YU

ZW GH GM KE LS MW SD SL SZ TZ UG ZW AM AZ BY KG KZ MD RU TJ TM AT BE CH

CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE BF BJ CF CG CI CM GA GN GW

ML MR NE SN TD TG

Publication Language: English

Fulltext Word Count: 5605

METHOD AND SYSTEM FOR AN EXTENSIBLE *MACRO* LANGUAGE
PROCEDE ET SYSTEME POUR *MACRO*-LANGAGE EXTENSIBLE

Main International Patent Class: *G06F-017/30*

Fulltext Availability:

Detailed Description

Claims

English Abstract

A method and system for an extensible *macro* language is provided. The system for providing the extensible language includes a parser and a *macro* handler (110) for processing *macro* commands not previously defined in the *macro* language. The parser (102) analyzes keywords in a *macro* language expression and recognizes one or more keywords (116) representing *macro* commands that were not previously defined in the *macro* language. The *macro* handler (110) receives the keyword in the *macro* expression and retrieves from a registry of keywords, an executable code or procedure (112) associated with the keyword. The executable code (112) is run to process the *macro* command represented by the keyword. The template language registry may be argumented to include any keywords and associated codes for extending the *macro* language.

French Abstract

L'invention concerne un procede et un systeme pour un *macro*-langage extensible. Le systeme destine a fournir le langage extensible comprend un analyseur syntaxique et un *macro*-module de traitement (110) destine a traiter des *macro*-commandes non definies prealablement dans le *macro*-langage. L'analyseur syntaxique (102) analyse des mots-cles dans une expression du *macro*-langage et il reconnait un ou plusieurs mots-cles (116) representant des *macro*-commandes n'ayant pas ete definies prealablement dans le *macro*-langage. Le *macro*-module (110)

determining based on a predetermined syntax of a *macro* language, one or more keywords in the analyzed *macro* language expression, the keyword representing a *macro* command not previously defined in the *macro* language; retrieving a code associated with the keyword from a registry of keywords; and executing the code associated with the keyword.

2 The method for providing an extensible *macro* language as claimed in claim 1, further comprising: extending the registry of keywords by inserting...

...and a code associated with the new keyword.

3 A system for providing an extensible *macro* language, comprising:
a parser having a predefined syntax to determine one or more extended keywords embedded within a *macro* language expression, the extended keyword representing a *macro* command undefined in a predetermined set of *macro* commands of a *macro* language;
a keyword repository having one or more keywords and associated codes; and
a *macro* handler coupled to the parser for receiving the extended keyword from the parser, the *macro* handler in response to the received extended keyword, retrieving a code associated with the received extended keyword from the keyword repository and executing the code to run the *macro* command represented by the extended keyword.

4 The extensible *macro* language as claimed in claim

3 wherein the keyword repository is augmented to include new keywords and associated codes.

5 A method for parsing a *macro* language expression, comprising:
analyzing a *macro* language expression; and
determining based on a predetermined syntax of a *macro* language, one or more keywords in the analyzed *macro* language expression, the keywords representing *macro* commands undefined in a predetermined set of *macro* commands of a *macro* language.

24/3,K/28 (Item 28 from file: 349)

DIALOG(R) File 349:PCT FULLTEXT

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00261261

METHOD AND SYSTEM FOR INTERFACING TO A TYPE LIBRARY
PROCEDE ET SYSTEME D'INTERFACAGE AVEC UNE BIBLIOTHEQUE DE TYPES

Patent Applicant/Assignee:

MICROSOFT CORPORATION,

Inventor(s):

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CARTER Alan W,

CORBETT Tom,

WOOLF Michael J,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9409428 A1 19940428

Application: WO 93US9673 19931008 (PCT/WO US9309673)

Priority Application: US 92959056 19921009

Designated States: CA JP AT BE CH DE DK ES FR GB GR IE IT LU MC NL PT SE

Publication Language: English
Fulltext Word Count: 13928

Main International Patent Class: *G06F-009/44*
Fulltext Availability:
Detailed Description
Claims

Detailed Description

... 3 is a block diagram showing sample components used when a word processor invokes a *macro* interpreter.

Figures 4. through 7 are block diagrams illustrating a preferred type library interface hierarchy...showing the hierarchy of an ITypeDesc object.

Figure 8 is a diagram showing steps a *macro* interpreter executes when interpreting the *macro* of Code Table 1,

Figure 9 is a flow diagram of the routine ExecuteGlobalFunction*

Figure...doc" that contains several paragraphs 102, The word processor has menu 103 for invoking a *macro* interpreter (an application). In this example, the *macro* interpreter is designed to run as part of the same process of the invoking computer program. The *macro* interpreter inputs statements in a C++@like language from a user and interprets the statements. When the user selects the *macro* menu 103,, *macro* window 104 is created. The user inputs the *macro* statements 105 and then selects the run menu of the *macro* window 104 to interpret the *macro* statements. In this example, the *macro* statements 105 cause each paragraph in the opened document to be right justified.

is Continuing...

...information, and documentation with the executable code of the word processor. The developer of the *macro* interpreter publishes a description of its C++-like language. The word processor and the *macro* interpreter are developed with no special knowledge of the other.

That is, the word processor is developed knowing only that it needs to load a *macro* interpreter stored in a file of a predefined name and invoke that *macro* interpreter when the user selects its *macro* menu. Similarly, the *macro* interpreter is developed knowing only that it needs to look to the type library that...

...passed when invoked to access the exposed objects of the invoking computer program. The same *macro* interpreter can be invoked by other kinds of computer programs, such as a spreadsheet or...

...of the exposed word processor objects and the published definition of the language of the *macro* interpreter to develop *macros* for the word processor. The *macro* interpreter inputs these *macros* and interacts with the word processor based on the information in the word processor type library to interpret the *macros*.

Figure 2 is a block diagram showing the generation of a type library. To generate...3 is a block diagram showing

the components used when the word processor invokes the *macro* interpreter. When a user selects the *macro* menu of the word processor 301, the word processor loads the *macro* interpreter code 302 in the address space of the word processor 301. In an alternate embodiment, the *macro* interpreter 302 could be loaded as a separate process and could use standard interprocess communication mechanisms to communicate with the word processor 301. Once the *macro* interpreter is invoked, the user then inputs *macro* statements 303. The *macro* interpreter 302 instantiates type library interface objects 304 to access the information in the word...

...contain the text and properties (e.g., justification) of the paragraphs. The CODE TABLE 1

```
*Macro* Justify
Document *pdoc
Paragraph *ppara
3 pdoc = GetDoco
i = 0
while (NULL != (ppara = pdoc->GetPara(i++))
ppara->SetJustify( 11rightfl)
EndMacro
```

Code Table I lists *macro* statements that set the justification of each paragraph in the open document. The numbers to the left of the statements are for reference. In line 1,, the *macro* declares the variable pdoc to point to a document object, In line 2, the *macro* declares the variable ppara to point to a paragraph object, In line 3, the *macro* sets the variable pdoc to point to the document object for the open document, The function GetDoc returns the pointer to the document object. In line 4. the *macro* sets the variable i to 0, The variable i is used to index through the paragraphs. In lines 5 and 6, the *macro* loops through each paragraph setting its justification to right ...SetJustify of the paragraph class sets the paragraph to right justified, The steps that the *macro* interpreter performs to interpret the *macro* of Code Table 1 are described in detail below.

Type libraries can also be used...

...returns a pointer to the instantiated object. The application can then access the object, The *macro* interpreter of the above example could instantiate an object a type paragraph, store data in...displays object type definitions within a type library to a user, For example, 5 the *macro* interpreter of the above example could have a -browsing mode in which it displayed the...GenerateFuncDesc(functionname, pfdesc);

```
ptdesc->SetFuncDesc(pfdesc)1;
```

Figure 8 is a diagram showing steps the *macro* interpreter executes to interpret the *macro* of Code Table 1, Figure 8 does not represent a flow diagram of the *macro* interpreter,, but rather represents those steps the *macro* interpreter executes when interpreting the *macro* of Code Table 1. In step 801, the *macro* interpreter opens the type library for the word processor. When the type library is opened,, a pointer to an ITypeLib object is returned in variable ptlib, In step 802, the *macro* interpreter allocates memory for the variable pdoc, which is a pointer to a document type. In step 803, the *macro* interpreter allocates memory for the variable ppara which is a pointer to a paragraph type. In step 804, the *macro*

interpreter interprets line 3 of Code Table 1, The *macro* interpreter invokes its routine ExecuteGlobalFunction passing it a pointer to the open type library, the...

...the document object for the open document in the word processor, In step 805,, the *macro* interpreter sets the variable pdoc to the return value. In step 806, the *macro* interpreter interprets line 4 of Code Table 1, The *macro* interpreter sets the variable i to 0. In steps 807 through 810, the *macro* interpreter interprets lines 5 and 6 of Code Table 1. In step 807, the *macro* interpreter invokes the routine ExecutememberFunction passing a pointer to the ITypeLib object for the open...

...returns a pointer to a paragraph object of the designated index. In step 808, the *macro* interpreter sets variable ppara equal to the return value, In step 809, if the variable ppara is equal to null, then all the paragraphs have been processed and the *macro* interpreter is done interpreting the Code Table 1, else the *macro* interpreter continues at step 810. In step 810, the *macro* interpreter invokes routine ExecuteFunctionMember passing a pointer to the ITypeLib object for the open libraryf...execute (SetJustify), and the parameter "right." The function member SetJustify has no return value. The *macro* interpreter then loops to step 807 to continue execution of the loop of lines 5...

Claim

... including the step of loading a second implementation of the predefined interface and wherein the *executing* computer program accesses the type definitions in a second type library *without* *recompiling* the computer program,

12 The method of claim 10 including the step of executing a...

24/AZ, TI/1 (Item from file: 348)
DIALOG(R) File 348: (c) 2003 European Patent Office. All rts. reserv.

01030324
MOBILE ELECTRONIC COMMERCE SYSTEM
MOBILES ELEKTRONISCHES HANDELSSYSTEM
SYSTEME DE COMMERCE ELECTRONIQUE MOBILE

24/AZ, TI/2 (Item 2 from file: 348)
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00952558
PROCESS CONTROL
PROZESSSTEUERVORRICHTUNG
COMMANDE DE PROCEDE

24/AZ, TI/3 (Item 3 from file: 348)
DIALOG(R) File 348: (c) 2003 European Patent Office. All rts. reserv.

00879292
Incremental compilation of c++ programs
Inkrementelle Komprimierung von C++ Programmen
Compilation incrementale de programmes C++

24/AZ, TI/4 (Item 4 from file: 348)
DIALOG(R) File 348: (c) 2003 European Patent Office. All rts. reserv.

00843962
Micoprocessor pipe control and register translation
Mikroprozessorpipelinesteuerung und Registerubersetzung
Commande de pipeline et traduction de registre pour microprocesseur

24/AZ, TI/5 (Item 5 from file: 348)
DIALOG(R) File 348: (c) 2003 European Patent Office. All rts. reserv.

00506416
INTEGRATED HIERARCHICAL REPRESENTATION OF COMPUTER PROGRAMS FOR A SOFTWARE
DEVELOPMENT SYSTEM
INTEGRIERTE HIERARCHISCHE DARSTELLUNG VON RECHNERPROGRAMMEN FUR EIN
SOFTWARE-ENTWICKLUNGSSYSTEM
REPRESENTATION HIERARCHIQUE INTEGREE DE PROGRAMMES INFORMATIQUES POUR UN
SYSTEME DE DEVELOPPEMENT DE LOGICIELS

24/AZ, TI/6 (Item 6 from file: 348)
DIALOG(R) File 348: (c) 2003 European Patent Office. All rts. reserv.

00431289
Incremental compiler for source code development system.
Inkrementeller Compiler fur ein Quellkodeentwicklungssystem.
Compileur incrementiel pour un systeme de developpement de code source.

24/AZ, TI/7 (Item 7 from file: 348)
DIALOG(R) File 348: (c) 2003 European Patent Office. All rts. reserv.

00402194
Simulation of selected logic circuit designs
Simulation von ausgewählten Logik-Schaltungsentwürfen
Simulation de conceptions selectionnees de circuit logique

24/AZ, TI/8 (Item 8 from file: 348)
DIALOG(R) File 348: (c) 2003 European Patent Office. All rts. reserv.

00281658
MULTIPROCESSING METHOD AND ARRANGEMENT.
MULTIPROZESSORVERFAHREN UND -ANORDNUNG.
PROCEDE ET AGENCEMENT MULTIPROCESSEURS.

24/AZ, TI/9 (Item 9 from file: 349)
DIALOG(R) File 349: (c) 2003 WIPO/Univentio. All rts. reserv.

01006405
DATA PROCESSING SYSTEM AND METHOD
SYSTEME ET PROCEDE DE TRAITEMENT DE DONNEES

24/AZ, TI/10 (Item 10 from file: 349)
DIALOG(R) File 349: (c) 2003 WIPO/Univentio. All rts. reserv.

00906109
INTEGRATED SYSTEM FOR BIOLOGICAL INFORMATION
SYSTEME INTEGRE POUR INFORMATIONS BIOLOGIQUES

24/AZ, TI/11 (Item 11 from file: 349)
DIALOG(R) File 349: (c) 2003 WIPO/Univentio. All rts. reserv.

00876811
SYSTEM, METHOD AND COMPUTER PROGRAM PRODUCT FOR DEVICE, OPERATING SYSTEM,
AND NETWORK TRANSPORT NEUTRAL SECURE INTERACTIVE MULTI-MEDIA MESSAGING
SYSTEME, PROCEDE ET PRODUIT PROGRAMME D'ORDINATEUR POUR APPAREIL, SYSTEME
D'EXPLOITATION ET MESSAGERIE MULTIMEDIA INTERACTIVE RESEAU, NEUTRE ET
SECURISEE

24/AZ, TI/12 (Item 12 from file: 349)
DIALOG(R) File 349: (c) 2003 WIPO/Univentio. All rts. reserv.

00802534
ANY-TO-ANY COMPONENT COMPUTING SYSTEM
SYSTEME INFORMATIQUE A COMPOSANTS TOUTE CATEGORIE

24/AZ, TI/13 (Item 13 from file: 349)
DIALOG(R) File 349: (c) 2003 WIPO/Univentio. All rts. reserv.

00790513
SOFTWARE DEVELOPMENT SYSTEM FOR FACILITATING SELECTION OF COMPONENTS
SYSTEME LOGICIEL DE MISE AU POINT POUR FACILITER LA SELECTION DE COMPOSANTS

24/AZ, TI/14 (Item 14 from file: 349)
DIALOG(R) File 349: (c) 2003 WIPO/Univentio. All rts. reserv.

00784143
SYSTEM, METHOD AND ARTICLE OF MANUFACTURE FOR LOAD BALANCING REQUESTS AMONG
SERVERS
SYSTEME, PROCEDE ET ARTICLE POUR EQUILIBREUR DE CHARGE DANS UN
ENVIRONNEMENT DE STRUCTURES DE SERVICES

24/AZ, TI/15 (Item 15 from file: 349)
DIALOG(R) File 349: (c) 2003 WIPO/Univentio. All rts. reserv.

00784138

SYSTEM, METHOD, AND ARTICLE OF MANUFACTURE FOR A REQUEST BATCHER IN A TRANSACTION SERVICES PATTERNS ENVIRONMENT
SYSTEME, PROCEDE ET ARTICLE MANUFACTURE POUR MODULE DE MISE EN LOTS DES REQUETES DANS UN ENVIRONNEMENT CARACTERISE PAR DES SERVICES TRANSACTIONNELS

24/AZ, TI/16 (Item 16 from file: 349)

DIALOG(R) File 349: (c) 2003 WIPO/Univentio. All rts. reserv.

00784136

A SYSTEM, METHOD AND ARTICLE OF MANUFACTURE FOR BUSINESS LOGIC SERVICES PATTERNS IN A NETCENTRIC ENVIRONMENT
SYSTEME, PROCEDE ET ARTICLE DE FABRICATION POUR STRUCTURES DE SERVICES DE LOGIQUE DE COMMERCE DANS UN ENVIRONNEMENT S'ARTICULANT AUTOUR DE L'INTERNET

24/AZ, TI/17 (Item 17 from file: 349)

DIALOG(R) File 349: (c) 2003 WIPO/Univentio. All rts. reserv.

00784134

A SYSTEM, METHOD AND ARTICLE OF MANUFACTURE FOR A CONSTANT CLASS COMPONENT IN A BUSINESS LOGIC SERVICES PATTERNS ENVIRONMENT
SYSTEME, PROCEDE ET ARTICLE MANUFACTURE UN COMPOSANT DE CLASSE DE CONSTANCE DANS UN ENVIRONNEMENT DE SCHEMAS DE SERVICES DE LOGIQUE D'AFFAIRES

24/AZ, TI/18 (Item 18 from file: 349)

DIALOG(R) File 349: (c) 2003 WIPO/Univentio. All rts. reserv.

00784131

A SYSTEM, METHOD AND ARTICLE OF MANUFACTURE FOR A MULTI-OBJECT FETCH COMPONENT IN AN INFORMATION SERVICES PATTERNS ENVIRONMENT
SYSTEME, PROCEDE ET ARTICLE MANUFACTURE POUR COMPOSANT DE RECUPERATION MULTI-OBJET DANS UN ENVIRONNEMENT CARACTERISE PAR DES SERVICES D'INFORMATIONS

24/AZ, TI/19 (Item 19 from file: 349)

DIALOG(R) File 349: (c) 2003 WIPO/Univentio. All rts. reserv.

00777016

A SYSTEM, METHOD AND ARTICLE OF MANUFACTURE FOR MAINTAINING DATA IN AN E-COMMERCE BASED TECHNICAL ARCHITECTURE
SYSTEME, PROCEDE ET ARTICLE MANUFACTURE DE MAINTIEN DES DONNEES DANS UNE ARCHITECTURE TECHNIQUE DE COMMERCE ELECTRONIQUE

24/AZ, TI/20 (Item 20 from file: 349)

DIALOG(R) File 349: (c) 2003 WIPO/Univentio. All rts. reserv.

00777011

A SYSTEM, METHOD AND ARTICLE OF MANUFACTURE FOR A CODES TABLE FRAMEWORK DESIGN IN AN E-COMMERCE ARCHITECTURE
SYSTEME, PROCEDE ET ARTICLE FABRIQUE POUR LA CONCEPTION D'UNE STRUCTURE DE TABLES DE CODES DANS UNE ARCHITECTURE DE COMMERCE ELECTRONIQUE

24/AZ, TI/21 (Item 21 from file: 349)

DIALOG(R) File 349: (c) 2003 WIPO/Univentio. All rts. reserv.

00772860

FAULT TOLERANT AND COMBINATORIAL SOFTWARE ENVIRONMENT SYSTEM, METHOD AND
MEDIUM
SYSTEME D'ENVIRONNEMENT LOGICIEL COMBINATOIRE ET INSENSIBLE AUX
DEFAILLANCES, ET PROCEDE ET SUPPORT ASSOCIES

24/AZ, TI/22 (Item 22 from file: 349)
DIALOG(R) File 349:(c) 2003 WIPO/Univentio. All rts. reserv.

00764607
SECURE, ACCOUNTABLE, MODULAR AND PROGRAMMABLE SOFTWARE TRAC
LOGICIEL TRAC PROGRAMMABLE, MODULAIRE, UTILISABLE ET SECURISE

24/AZ, TI/23 (Item 23 from file: 349)
DIALOG(R) File 349:(c) 2003 WIPO/Univentio. All rts. reserv.

00560546
METHOD AND SYSTEM FOR AN EXTENSIBLE *MACRO* LANGUAGE
PROCEDE ET SYSTEME POUR *MACRO*-LANGAGE EXTENSIBLE

24/AZ, TI/24 (Item 24 from file: 349)
DIALOG(R) File 349:(c) 2003 WIPO/Univentio. All rts. reserv.

00551259
VIRTUAL FILE SERVER
SERVEUR DE FICHIERS VIRTUELS

24/AZ, TI/25 (Item 25 from file: 349)
DIALOG(R) File 349:(c) 2003 WIPO/Univentio. All rts. reserv.

00518042
APPLICATION PROGRAM INTERFACES IN AN OPERATING SYSTEM
INTERFACES DE PROGRAMME D'APPLICATION DANS UN SYSTEME D'EXPLOITATION

24/AZ, TI/26 (Item 26 from file: 349)
DIALOG(R) File 349:(c) 2003 WIPO/Univentio. All rts. reserv.

00376923
STRUCTURED FOCUSED HYPERTEXT DATA STRUCTURE
STRUCTURE DE DONNEES HYPERTEXTE ARTICULEE SUR LA STRUCTURATION

24/AZ, TI/27 (Item 27 from file: 349)
DIALOG(R) File 349:(c) 2003 WIPO/Univentio. All rts. reserv.

00348333
AN INTEGRATED DEVELOPMENT PLATFORM FOR DISTRIBUTED PUBLISHING AND
MANAGEMENT OF HYPERMEDIA OVER WIDE AREA NETWORKS
PLATE-FORME DE DEVELOPPEMENT INTEGREE POUR LA PUBLICATION ET LA GESTION
REPARTIES D'HYPERMEDIA SUR DES RESEAUX LONGUE PORTEE

24/AZ, TI/28 (Item 28 from file: 349)
DIALOG(R) File 349:(c) 2003 WIPO/Univentio. All rts. reserv.

00261261
METHOD AND SYSTEM FOR INTERFACING TO A TYPE LIBRARY
PROCEDE ET SYSTEME D'INTERFACAGE AVEC UNE BIBLIOTHEQUE DE TYPES

24/AZ, TI/29 (Item 29 from file: 349)

DIALOG(R)File 349:(c) 2003 WIPO/Univentio. All rts. reserv.

00234265.

SYSTEM FOR DIVIDING PROCESSING TASKS INTO SIGNAL PROCESSOR AND
DECISION-MAKING MICROPROCESSOR INTERFACING
SYSTEME DE SEPARATION DES TACHES DE TRAITEMENT EN TACHES POUR INTERFACAGE
AVEC UN PROCESSEUR DE SIGNAUX ET UN MICROPROCESSEUR DE PRISE DE
DECISION

24/AZ, TI/30 (Item 30 from file: 349)

DIALOG(R)File 349:(c) 2003 WIPO/Univentio. All rts. reserv.

00149051

MULTIPROCESSING METHOD AND ARRANGEMENT
PROCEDE ET AGENCEMENT A MULTIPROCESSEUR

```
?show files;ds
File 35:Dissertation Abs Online 1861-2003/Oct
(c) 2003 ProQuest Info&Learning
File 65:Inside Conferences 1993-2003/Dec W1
(c) 2003 BLDSC all rts. reserv.
File 8: Ei Compendex(R) 1970-2003/Nov W5
(c) 2003 Elsevier Eng. Info. Inc.
File 202:Info. Sci. & Tech. Abs. 1966-2003/Nov 17
(c) 2003 EBSCO Publishing
File 2:INSPEC 1969-2003/Nov W5
(c) 2003 Institution of Electrical Engineers
File 94:JICST-EPlus 1985-2003/Dec W1
(c)2003 Japan Science and Tech Corp(JST)
File 6:NTIS 1964-2003/Dec W1
(c) 2003 NTIS, Intl Cpyrght All Rights Res
File 144:Pascal 1973-2003/Nov W5
(c) 2003 INIST/CNRS
File 34:SciSearch(R) Cited Ref Sci 1990-2003/Nov W5
(c) 2003 Inst for Sci Info
File 434:SciSearch(R) Cited Ref Sci 1974-1989/Dec
(c) 1998 Inst for Sci Info
File 256:SoftBase:Reviews,Companies&Prods. 82-2003/Nov
(c)2003 Info.Sources Inc
File 99:Wilson Appl. Sci & Tech Abs 1983-2003/Oct
(c) 2003 The HW Wilson Co.
File 111:TGG Natl.Newspaper Index(SM) 1979-2003/Dec 08
(c) 2003 The Gale Group
```

Set	Items	Description
S1	3986691	EXTEN? OR UNDEFINED OR (NON OR "NOT" OR NO OR WITHOUT OR WITH()OUT OR OMIT? ? OR OMITT???) (3W) (DEFIN? OR DESIGNAT??? OR STIPULAT??? OR SET OR DETERMIN? OR ESTABLISH?) OR ADD??? OR ADDITIONAL
S2	58590	MACRO? ? OR MACROINSTRUCTION? ?
S3	3505431	COMMAND? ? OR INSTRUCTION? ? OR ELEMENT? ? OR ARGUMENT? ?
S4	17542033	RUN? ? OR RUNNING OR EXECUT??? OR PERFORM? OR ACTION? OR ACTIVAT??? OR BEGIN? OR EFFECT??? OR ACTUAT??? OR LAUNCH OR INITIAT???
S5	245	(NON OR "NOT" OR NO OR WITHOUT OR WITH()OUT OR OMIT? ? OR OMITT???) (3N) (RECOMPIL??? OR REOPTIMIS??? OR REOPTIMIZ??? OR RE() (COMPIL??? OR OPTIMIS??? OR OPTIMIZ???)
S6	44146	KEYWORD? ? OR KEY() (WORD? ? OR TEXT OR PHRASE? ?)
S7	1484514	REGISTR??? OR REGISTER??? OR RECORD??? OR TABULAT???
S8	3605476	ON(2W) (REQUEST OR DEMAND OR FLY) OR (AS OR WHEN) (2W) (NEEDED OR REQUIRED) OR JUST(2W) TIME OR DYNAMIC? OR JIT OR TO() ORDER OR REALTIME OR (REAL OR ACTUAL) () TIME OR BEHIND(2W) SCENE? ? OR ADAPTIV? OR TRANSPARENT?
S9	86	S2(10N) (S1(7N)S3)
S10	45	S4(5N)S5
S11	6	S6(5N)S7(5N)S8
S12	0	S9(S)S10(S)S11
S13	1573	S1(10N)S2
S14	0	S13(S)S10
S15	1	S13(S) ((S4(S)S5) OR (S6(S)S7(S)S8))
S16	0	S9 AND S10
S17	1	S2 AND (S4(S)S5)
S18	137	S9 OR S10 OR S11
S19	103	S18 NOT PY>1998
S20	102	S19 NOT PD=19981017:20040131
S21	77	RD (unique items)
S22	40	S2(5N) (S1(3N)S3)
S23	31	S4(3N)S5
S24	77	S11 OR S22 OR S23
S25	59	S24 NOT PY>1998
S26	59	S25 NOT PD=19981017:20040131

?show files;ds

File 347:JAPIO Oct 1976-2003/Aug(Updated 031202)
(c) 2003 JPO & JAPIO

File 348:EUROPEAN PATENTS 1978-2003/Nov W05
(c) 2003 European Patent Office

File 349:PCT FULLTEXT 1979-2002/UB=20031203,UT=20031127
(c) 2003 WIPO/Univentio

File 350:Derwent WPIX 1963-2003/UD,UM &UP=200379
(c) 2003 Thomson Derwent

File 371:French Patents 1961-2002/BOPI 200209
(c) 2002 INPI. All rts. reserv.

File 120:U.S. Copyrights 1978-2003/Dec 02
(c) format only 2003 The Dialog Corp.

File 426:LCMARC-Books 1968-2003/Dec W1
(c) format only 2003 Dialog Corporation

File 430:British Books in Print 2003/Nov W4
(c) 2003 J. Whitaker & Sons Ltd.

File 35:Dissertation Abs Online 1861-2003/Oct
(c) 2003 ProQuest Info&Learning

File 65:Inside Conferences 1993-2003/Dec W1
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File 202:Info. Sci. & Tech. Abs. 1966-2003/Nov 17
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File 2:INSPEC 1969-2003/Nov W5
(c) 2003 Institution of Electrical Engineers

File 94:JICST-EPlus 1985-2003/Dec W1
(c)2003 Japan Science and Tech Corp(JST)

File 6:NTIS 1964-2003/Dec W1
(c) 2003 NTIS, Intl Cpyrght All Rights Res

File 144:Pascal 1973-2003/Nov.W5
(c) 2003 INIST/CNRS

File 34:SciSearch(R) Cited Ref Sci 1990-2003/Nov W5
(c) 2003 Inst for Sci Info

File 434:SciSearch(R) Cited Ref Sci 1974-1989/Dec
(c) 1998 Inst for Sci Info

File 256:SoftBase:Reviews,Companies&Prods. 82-2003/Nov
(c)2003 Info.Sources Inc

File 99:Wilson Appl. Sci & Tech Abs 1983-2003/Oct
(c) 2003 The HW Wilson Co.

File 111:TGG Natl.Newspaper Index(SM) 1979-2003/Dec 08
(c) 2003 The Gale Group

File 9:Business & Industry(R) Jul/1994-2003/Dec 08
(c) 2003 Resp. DB Svcs.

File 15:ABI/Inform(R) 1971-2003/Dec 09
(c) 2003 ProQuest Info&Learning

File 635:Business Dateline(R) 1985-2003/Dec 09
(c) 2003 ProQuest Info&Learning

File 610:Business Wire 1999-2003/Dec 09
(c) 2003 Business Wire.

File 647:CMP. Computer Fulltext 1988-2003/Dec W1
(c) 2003 CMP Media, LLC

File 674:Computer News Fulltext 1989-2003/Dec W1
(c) 2003 IDG Communications

File 275:Gale Group Computer DB(TM) 1983-2003/Dec 08
(c) 2003 The Gale Group

File 20:Dialog Global Reporter 1997-2003/Dec 09
(c) 2003 The Dialog Corp.

File 98:General Sci Abs/Full-Text 1984-2003/Oct
(c) 2003 The HW Wilson Co.

File 233:Internet & Personal Comp. Abs. 1981-2003/Jul
(c) 2003, EBSCO Pub.

File 624:McGraw-Hill Publications 1985-2003/Dec 09
(c) 2003 McGraw-Hill Co. Inc

File 621:Gale Group New Mod. Annou. (R) 1985-2003/Dec 08
 (c) 2003 The Gale Group
 File 636:Gale Group Newsletter DB(TM) 1987-2003/Dec 08
 (c) 2003 The Gale Group
 File 369:New Scientist 1994-2003/Nov W5
 (c) 2003 Reed Business Information Ltd.
 File 483:Newspaper Abs Daily 1986-2003/Dec 08
 (c) 2003 ProQuest Info&Learning
 File 484:Periodical Abs Plustext 1986-2003/Nov W5
 (c) 2003 ProQuest
 File 613:PR Newswire 1999-2003/Dec 09
 (c) 2003 PR Newswire Association Inc
 File 16:Gale Group PROMT(R) 1990-2003/Dec 08
 (c) 2003 The Gale Group
 File 160:Gale Group PROMT(R) 1972-1989
 (c) 1999 The Gale Group
 File 634:San Jose Mercury Jun 1985-2003/Dec 08
 (c) 2003 San Jose Mercury News
 File 370:Science 1996-1999/Jul W3
 (c) 1999 AAAS
 File 95:TEME-Technology & Management 1989-2003/Nov W4
 (c) 2003 FIZ TECHNIK
 File 148:Gale Group Trade & Industry DB 1976-2003/Dec 08
 (c) 2003 The Gale Group
 File 553:Wilson Bus. Abs. FullText 1982-2003/Oct
 (c) 2003 The HW Wilson Co
 File 47:Gale Group Magazine DB(TM) 1959-2003/Dec 08
 (c) 2003 The Gale group
 File 141:Readers Guide 1983-2003/Oct
 (c) 2003 The HW Wilson Co

Set	Items	Description
S1	16	AU='DEFFLER T':AU='DEFFLER TAD A'
S2	0	AU='DEFFLER, T'
S3	306	AU='MINTZ E':AU='MINTZ EE'
S4	42	AU='MINTZ EM':AU='MINTZ ERIC M'
S5	32	AU='MINTZ, E.':AU='MINTZ, E.A.'
S6	52	AU='MINTZ, ERIC':AU='MINTZ, ERIC, 1952-'
S7	445	S1 OR S2 OR S3 OR S4 OR S5 OR S6
S8	41	S7 FROM 347,348,349,350,371
S9	444755	IC=(G06F-017? OR G06F-007? OR G06F-009?)
S10	19	S8 AND S9
S11	283933	MACRO? ? OR MACROINSTRUCTION? ?
S12	7	S10 AND S11
S13	7	S8 AND S11
S14	7	IDPAT S12 (sorted in duplicate/non-duplicate order)
S15	404	S7 NOT S8
S16	0	S11 AND S15
S17	9352764	ON(2W) (REQUEST OR DEMAND) OR ON(2W) FLY OR (AS OR WHEN) (2W) - (NEEDED OR REQUIRED) OR JUST(2W) TIME OR DYNAMIC? OR JIT OR TO- () ORDER OR REALTIME OR (REAL OR ACTUAL) () TIME OR BEHIND(2W) SC- ENE? ? OR ADAPTIV? OR TRANSPARENT?
S18	65986	S11 AND S17
S19	8	S15 AND S17
S20	5	S19 NOT PY>1998
S21	4	RD (unique items)
S22	11	S14 OR S21

22/3,K/1 (Item 1 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
(c) 2003 European Patent Office. All rts. reserv.

01161802

METHOD AND SYSTEM FOR AN EXTENSIBLE *MACRO* LANGUAGE
VERFAHREN UND SYSTEM FUR EXTENSIBLE MAGRO-SPRACHE
PROCEDE ET SYSTEME POUR *MACRO*-LANGAGE EXTENSIBLE
PATENT ASSIGNEE:

Computer Associates Think, Inc., (2947530), 1 Computer Associates Plaza,
Islandia, New York 11749, (US), (Applicant designated States: all)

INVENTOR:

DEFFLER, Tad, A., 457 Rockaway Street, Boonton, NJ 07005, (US)

MINTZ, Eric, 131 Woodbridge Avenue, Metuchen, NJ 08840-2030, (US)

LEGAL REPRESENTATIVE:

Whitten, George Alan (71691), R.G.C. Jenkins & Co., 26 Caxton Street,
London SW1H 0RJ, (GB)

PATENT (CC, No, Kind, Date): EP 1121654 A1 010808 (Basic)
WO 200023919 000427

APPLICATION (CC, No, Date): EP 99956567 991015; WO 99US24115 991015

PRIORITY (CC, No, Date): US 104682 P 981016

DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI;
LU; MC; NL; PT; SE

INTERNATIONAL PATENT CLASS: *G06F-017/30*

NOTE:

No A-document published by EPO

LANGUAGE (Publication,Procedural,Application): English; English; English

METHOD AND SYSTEM FOR AN EXTENSIBLE *MACRO* LANGUAGE
PROCEDE ET SYSTEME POUR *MACRO*-LANGAGE EXTENSIBLE
INVENTOR:

DEFFLER, Tad, A....

...US)

MINTZ, Eric...

INTERNATIONAL PATENT CLASS: *G06F-017/30*

22/3,K/4 (Item 3 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
(c) 2003 WIPO/Univentio. All rts. reserv.

00560510 **Image available**

APPARATUS AND METHOD FOR BUILDING MODELING TOOLS
APPAREIL ET PROCEDE DE CONSTRUCTION D'OUTILS DE MODELISATION

Patent Applicant/Assignee:

COMPUTER ASSOCIATES THINK INC,
DEFFLER Tad A,

Inventor(s):

DEFFLER Tad A

Patent and Priority Information (Country, Number, Date):

Patent: WO 200023883 A1 20000427 (WO 0023883)

Application: WO 99US24118 19991015 (PCT/WO US9924118)

Priority Application: US 98104682 19981016

Designated States: AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES
FI GB GE GH GM HU ID IL IS JP KE KG KR KZ LK LR LS LT LU LV MD MG MK MN
MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG US UZ VN YU
ZW GH GM KE LS MW SD SL SZ TZ UG ZW AM AZ BY KG KZ MD RU TJ TM AT BE CH
CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE BF BJ CF CG CI CM GA GN GW
ML MR NE SN TD TG

Publication Language: English

Fulltext Word Count: 6209

Inventor(s):

DEFFLER Tad A...

Main International Patent Class: *G06F-009/44*

Fulltext Availability:
Detailed Description

Detailed Description

... S. Patent Application No. (Attorney Docket
#22074661-25533) entitled METHOD AND SYSTEM FOR AN
EXTENSIBLE *MACRO* LANGUAGE, being concurrently filed on
the same day, which is incorporated by reference herein
in...

22/3,K/9 (Item 1 from file: 2)

DIALOG(R)File 2:INSPEC

(c) 2003 Institution of Electrical Engineers. All rts. reserv.

03863497 INSPEC Abstract Number: C91029629

Title: An approach to efficient software/hardware integration in embedded
computer systems

Author(s): Kestenbergs, N.; Kadary, V.; Fisher, R.; *Mintz, E.*

Author Affiliation: MBT/Israel Aircraft Ind., Yehud, Israel

Conference Title: Proceedings. Third Israel Conference on Computer
Systems and Software Engineering p.24-9

Publisher: IEEE, Washington, DC, USA

Publication Date: 1988 Country of Publication: USA vii+188 pp.

ISBN: 0 8186 0884 6

Conference Sponsor: IEEE

Conference Date: 6-7 June 1988 Conference Location: Tel-Aviv, Israel

Language: English

Subfile: C

Author(s): Kestenbergs, N.; Kadary, V.; Fisher, R.; *Mintz, E.*

Descriptors: *real*-time systems...

22/AA,AN,AZ,TI/1 (Item 1 from file: 348)
DIALOG(R)File 348:(c) 2003 European Patent Office. All rts. reserv.

01161802

METHOD AND SYSTEM FOR AN EXTENSIBLE *MACRO* LANGUAGE
VERFAHREN UND SYSTEM FUR EXTENSIBLE MAGRO-SPRACHE
PROCEDE ET SYSTEME POUR *MACRO*-LANGAGE EXTENSIBLE
APPLICATION (CC, No, Date): EP 99956567 991015; WO 99US24115 991015
PRIORITY (CC, No, Date): US 104682 P 981016

22/AA,AN,AZ,TI/2 (Item 1 from file: 349)
DIALOG(R)File 349:(c) 2003 WIPO/Univentio. All rts. reserv.

00560546

METHOD AND SYSTEM FOR AN EXTENSIBLE *MACRO* LANGUAGE
PROCEDE ET SYSTEME POUR *MACRO*-LANGAGE EXTENSIBLE
Application: WO 99US24115 19991015 (PCT/WO US9924115)

22/AA,AN,AZ,TI/3 (Item 2 from file: 349)
DIALOG(R)File 349:(c) 2003 WIPO/Univentio. All rts. reserv.

00560544

ACCESSING A HIERARCHICAL DATA STORE THROUGH AN SQL INPUT
ACCES A UNE MEMOIRE DE DONNEES HIERARCHIQUE PAR UNE ENTREE SQL
Application: WO 99US23876 19991015 (PCT/WO US9923876)

22/AA,AN,AZ,TI/4 (Item 3 from file: 349)
DIALOG(R)File 349:(c) 2003 WIPO/Univentio. All rts. reserv.

00560510

APPARATUS AND METHOD FOR BUILDING MODELING TOOLS
APPAREIL ET PROCEDE DE CONSTRUCTION D'OUTILS DE MODELISATION
Application: WO 99US24118 19991015 (PCT/WO US9924118)

22/AA,AN,AZ,TI/5 (Item 4 from file: 349)
DIALOG(R)File 349:(c) 2003 WIPO/Univentio. All rts. reserv.

00560509

IMPACT ANALYSIS OF A MODEL
ANALYSE D'IMPACT D'UN MODELE
Application: WO 99US24117 19991015 (PCT/WO US9924117)

22/AA,AN,AZ,TI/6 (Item 5 from file: 349)
DIALOG(R)File 349:(c) 2003 WIPO/Univentio. All rts. reserv.

00560490

DETERMINING DIFFERENCES BETWEEN TWO OR MORE METADATA MODELS
PROCEDE DE DETERMINATION DES DIFFERENCES ENTRE DEUX MODELES OU DAVANTAGE
Application: WO 99US24120 19991015 (PCT/WO US9924120)

22/AA,AN,AZ,TI/7 (Item 1 from file: 350)
DIALOG(R)File 350:(c) 2003 Thomson Derwent. All rts. reserv.

013193070

WPI Acc No: 2000-364943/
Extensible *macro* language providing method for use in computer language
processors, involves retrieving code associated with keywords
representing new *macro* command, which is then executed
Local Applications (No Type Date): WO 99US24115 A 19991015; AU 200013152 A

19991015; EP 99956567 19991015; WO 99US24115 A 19991015; BR 9914551 A
19991015; WO 99US24115 A 19991015; WO 99US24115 A 19991015; JP 2000577592
A 19991015; CN 99812038 A 19991015
Priority Applications (No Type Date): US 98104682 P 19981016

22/AA,AN,AZ,TI/8 (Item 1 from file: 8)
DIALOG(R)File 8:(c) 2003 Elsevier Eng. Info. Inc. All rts. reserv.

02227281
E.I. Monthly No: EIM8702-008303
Title: *ADAPTIVE* SUBBANDS EXCITED TRANSFORM (ASET) CODING.

22/AA,AN,AZ,TI/9 (Item 1 from file: 2)
DIALOG(R)File 2:(c) 2003 Institution of Electrical Engineers. All rts.
reserv.

03863497 INSPEC Abstract Number: C91029629
Title: An approach to efficient software/hardware integration in embedded
computer systems

22/AA,AN,AZ,TI/10 (Item 2 from file: 2)
DIALOG(R)File 2:(c) 2003 Institution of Electrical Engineers. All rts.
reserv.

02867648 INSPEC Abstract Number: B87027341
Title: *Adaptive* subbands excited transform (ASET) coding (speech coding)

22/AA,AN,AZ,TI/11 (Item 1 from file: 144)
DIALOG(R)File 144:(c) 2003 INIST/CNRS. All rts. reserv.

04772192 PASCAL No.: 83-0012655
Actinide tris(hydrocarbyls). Synthesis, properties, structure, and
molecular *dynamics* of thorium and uranium pentamethylcyclopentadienyl
tris(eta SUP n -benzyls)

?show files;ds

File 347:JAPIO Oct 1976-2003/Aug(Updated 031202)

(c) 2003 JPO & JAPIO

File 350:Derwent WPIX 1963-2003/UD,UM &UP=200379

(c) 2003 Thomson Derwent

File 371:French Patents 1961-2002/BOPI 200209

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Set	Items	Description
S1	2682317	EXTEN? OR UNDEFINED OR (NON OR "NOT" OR NO OR WITHOUT OR WITH() OUT OR OMIT? ? OR OMITT???) (3W) (DEFIN? OR DESIGNAT??? OR STIPULAT??? OR SET OR DETERMIN? OR ESTABLISH?) OR ADD??? OR A- DDITIONAL
S2	12262	MACRO? ? OR MACROINSTRUCTION? ?
S3	2201747	COMMAND? ? OR INSTRUCTION? ? OR ELEMENT? ? OR ARGUMENT? ?
S4	4591899	RUN? ? OR RUNNING OR EXECUT??? OR PERFORM? OR ACTION? OR A- CTIVAT??? OR BEGIN? OR EFFECT??? OR ACTUAT??? OR LAUNCH OR IN- ITIAT???
S5	123	(NON OR "NOT" OR NO OR WITHOUT OR WITH() OUT OR OMIT? ? OR - OMITT???) (3N) (RECOMPIL??? OR REOPTIMIS??? OR REOPTIMIZ??? OR - RE() (COMPIL??? OR OPTIMIS??? OR OPTIMIZ???)
S6	9447	KEYWORD? ? OR KEY() (WORD? ? OR TEXT OR PHRASE? ?)
S7	1502829	REGISTR??? OR REGISTER??? OR RECORD??? OR TABULAT???
S8	646997	ON(2W) (REQUEST OR DEMAND OR FLY) OR (AS OR WHEN) (2W) (NEEDED OR REQUIRED) OR JUST(2W) TIME OR DYNAMIC? OR JIT OR TO() ORDER OR REALTIME OR (REAL OR ACTUAL) () TIME OR BEHIND(2W) SCENE? ? OR ADAPTIV? OR TRANSPARENT?
S9	54	S2(10N) (S1(7N) S3)
S10	25	S4(5N) S5
S11	9	S6(5N) S7(5N) S8
S12	0	S9(S) S10(S) S11
S13	21	S6(10N) S7(10N) S8
S14	0	S9 AND S10 AND S13
S15	0	S2 AND S13
S16	0	S9 AND (S10 OR S13)
S17	0	S2 AND (S10 OR S13)
S18	2	S2 AND S4 AND S5
S19	62194	S1(5N) (S2 OR S3)
S20	1	S2 AND ((S4(S) S5) OR (S6(S) S7(S) S8))
S21	40	S2(7N) (S1(5N) S3)
S22	76	S10 OR S11 OR S18 OR S20 OR S21
S23	387515	IC=(G06F-017? OR G06F-007? OR G06F-009?)
S24	32	S22 AND S23
S25	14	S21 AND S23
S26	12	S10 AND S23
S27	37	S26 OR S11 OR S18 OR S20 OR S25
S28	32	IDPAT S24 (sorted in duplicate/non-duplicate order)
S29	32	IDPAT S24 (primary/non-duplicate records only)

29/3,K/5 (Item 5 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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014986102 **Image available**
WPI Acc No: 2003-046617/200304
XRPX Acc No: N03-036797

File format translator writes set of features of source file into target
file in target format using back-end lookup table

Patent Assignee: STEVENS M A (STEV-I)

Inventor: STEVENS M A

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 20020143823	A1	20021003	US 2001766335	A	20010119	200304 B

Priority Applications (No Type Date): US 2001766335 A 20010119

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 20020143823	A1	17	G06F-017/22	

Abstract (Basic):

... Performs *effective* translation of distinct file format
without *re*-*compiling* the source file, using the lookup tables...

International Patent Class (Main): *G06F-017/22*

International Patent Class (Additional): *G06F-017/21*

29/3,K/7 (Item 7 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2003 Thomson Derwent. All rts. reserv.

014802937 **Image available**
WPI Acc No: 2002-623643/200267
XRPX Acc No: N02-493946

Compile system has subroutine call management unit which replaces object
code with executable program, such that object code is loaded during
program execution operation

Patent Assignee: NEC CORP (NIDE)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
JP 2002222088	A	20020809	JP 200117287	A	20010125	200267 B

Priority Applications (No Type Date): JP 200117287 A 20010125

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
JP 2002222088	A	21	G06F-009/45	

Abstract (Basic):

... Enables *re*-*compiling* *without* reducing *execution* speed by
providing the subroutine call management unit which replaces the object
code with the...

International Patent Class (Main): *G06F-009/45*

29/3,K/10 (Item 10 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2003 Thomson Derwent. All rts. reserv.

013242800 **Image available**
WPI Acc No: 2000-414682/200036
XRPX Acc No: N00-309844

Method for modifying executable files including several compiled
instructions by detecting whether instruction is patch instruction which

transfers flow of execution to loaded associated converted instruction group

Patent Assignee: APPLIED MICROSYSTEMS CORP (MICR-N); APPLIED MICROSYSTEMS INC (MICR-N); TINKER J L (TINK-I)

Inventor: TINKER J L

Number of Countries: 028 Number of Patents: 004

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
EP 1014263	A2	20000628	EP 99310012	A	19991213	200036 B
JP 2000181725	A	20000630	JP 99355196	A	19991214	200037
CA 2292123	A1	20000614	CA 2292123	A	19991208	200044
US 20020073398	A1	20020613	US 98212182	A	19981214	200243

Priority Applications (No Type Date): US 98212182 A 19981214

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
EP 1014263	A2	E	24	G06F-009/445	
Designated States (Regional): AL AT BE CH CY DE DK ES FI FR GB GR IE IT					
LI LT LU LV MC MK NL PT RO SE SI					
JP 2000181725	A		26	G06F-009/455	
CA 2292123	A1	E		G06F-009/44	
US 20020073398	A1			G06F-009/44	

Abstract (Basic):

... INDEPENDENT CLAIMS are included for: a computer system for adding instructions to be *executed* to executable compiled file *without* its *recompiling* and a computer readable medium containing instructions for controlling a computer system for adding instructions to be *executed* to executable compiled file *without* its *recompiling*.

International Patent Class (Main): *G06F-009/44*...

...*G06F-009/445*...

...*G06F-009/455*

International Patent Class (Additional): *G06F-009/45*...

29/3,K/11 (Item 11 from file: 350)

DIALOG(R)File 350:Derwent WPIX

(c) 2003 Thomson Derwent. All rts. reserv.

013193070 **Image available**

WPI Acc No: 2000-364943/200031

Related WPI Acc No: 2000-350445; 2000-364925; 2000-364933; 2000-364941; 2002-506611

XRPX Acc No: N00-273130

Extensible macro language providing method for use in computer language processors, involves retrieving code associated with keywords representing new macro command, which is then executed

Patent Assignee: COMPUTER ASSOC THINK INC (COMP-N)

Inventor: DEFFLER T A; MINTZ E

Number of Countries: 081 Number of Patents: 006

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
WO 200023919	A1	20000427	WO 99US24115	A	19991015	200031 B
AU 200013152	A	20000508	AU 200013152	A	19991015	200037
EP 1121654	A1	20010808	EP 99956567	A	19991015	200146
			WO 99US24115	A	19991015	
BR 9914551	A	20020305	BR 9914551	A	19991015	200225
			WO 99US24115	A	19991015	
JP 2002528794	W	20020903	WO 99US24115	A	19991015	200273
			JP 2000577592	A	19991015	
CN 1361891	A	20020731	CN 99812038	A	19991015	200279

Priority Applications (No Type Date): US 98104682 P 19981016

Patent Details:

Patent No Kind Lan Pg Main IPC Filing Notes

WO 200023919 A1 E 31 G06F-017/30

Designated States (National): AL AM AT AU AZ BA BB BG BR BY CA CH CN CU
CZ DE DK EE ES FI GB GE GH GM HU ID IL IS JP KE KG KR KZ LK LR LS LT LU
LV MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA
UG US UZ VN YU ZW

Designated States (Regional): AT BE CH CY DE DK EA ES FI FR GB GH GM GR
IE IT KE LS LU MC MW NL OA PT SD SE SL SZ TZ UG ZW

AU 200013152 A G06F-017/30 Based on patent WO 200023919

EP 1121654 A1 E G06F-017/30 Based on patent WO 200023919

Designated States (Regional): AT BE CH CY DE DK ES FI FR GB GR IE IT LI
LU MC NL PT SE

BR 9914551 A G06F-017/30 Based on patent WO 200023919

JP 2002528794 W 20 G06F-009/45 Based on patent WO 200023919

CN 1361891 A G06F-017/30

Abstract (Basic):

... The providing method involves determining one or more keywords
representing new *macro* *command* *not* previously *defined* in the
macro language, in the analyzed *macro* language expression, based on
preset syntax of the macro language. Then, code associated with the...

International Patent Class (Main): *G06F-009/45*...

...*G06F-017/30*

29/3,K/21 (Item 21 from file: 347)

DIALOG(R)File 347:JAPIO

(c) 2003 JPO & JAPIO. All rts. reserv.

06948137 **Image available**

DEVICE AND METHOD FOR ADAPTIVE DICTIONARY

PUB. NO.: 2001-175689 [JP 2001175689 A]

PUBLISHED: June 29, 2001 (20010629)

INVENTOR(s): KANEMICHI TOSHIKI
YOSHIDA HIDEYUKI
WATANABE TAISUKE

APPLICANT(s): MATSUSHITA ELECTRIC IND CO LTD

APPL. NO.: 2000-341565 [JP 2000341565]
Division of 08-230012 [JP 96230012]

FILED: August 30, 1996 (19960830)

PRIORITY: 08-031547 [JP 9631547], JP (Japan), February 20, 1996
(19960220)

07-226172 [JP 95226172], JP (Japan), September 04, 1995
(19950904)

INTL CLASS: *G06F-017/30*; G06N-003/00

ABSTRACT

PROBLEM TO BE SOLVED: To provide an *adaptive* dictionary device which
preferentially *registers* a *key* *word* effective for the purpose of
deciding whether a user is necessary or not by using...

29/3,K/31 (Item 31 from file: 347)

DIALOG(R)File 347:JAPIO

(c) 2003 JPO & JAPIO. All rts. reserv.

02021846 **Image available**

DATA PROCESSOR

PUB. NO.: 61-235946 [JP 61235946 A]
PUBLISHED: October 21, 1986 (19861021)
INVENTOR(s): SASAKI HISAO
MAEJIMA HIDEO
HOTTA TAKASHI
APPLICANT(s): HITACHI LTD [000510] (A Japanese Company or Corporation), JP
(Japan)
APPL. NO.: 60-076565 [JP 8576565]
FILED: April 12, 1985 (19850412)
JOURNAL: Section: P, Section No. 555, Vol. 11, No. 79, Pg. 57, March
11, 1987 (19870311)
INTL CLASS: *G06F-009/22*

ABSTRACT

... decoder so that a register selecting signal is generated. Accordingly, in case of changing and *adding* a *macro*-instruction* having *no* register *designating* field, it can be coped with by only rewriting a micro-instruction stored in a...

29/AZ, TI/1 (Item 1 from file: 350)
DIALOG(R) File 350:(c) 2003 Thomson Derwent. All rts. reserv.

015684803

Pipeline microprocessor has execution logic that issues transaction over memory bus that requests for cache line in exclusive state, on receiving micro-instruction from translation logic

29/AZ, TI/2 (Item 2 from file: 350)
DIALOG(R) File 350:(c) 2003 Thomson Derwent. All rts. reserv.

015604869

Simulation apparatus for hardware design verification, includes auxiliary simulation model that generates pseudo output data without performing real arithmetic processing with respect to input data

29/AZ, TI/3 (Item 3 from file: 350)
DIALOG(R) File 350:(c) 2003 Thomson Derwent. All rts. reserv.

015377184

Pipeline microprocessor includes floating point register logic which performs floating point exchange operation directed by micro instruction extension, in parallel with operation directed by micro instruction

29/AZ, TI/4 (Item 4 from file: 350)
DIALOG(R) File 350:(c) 2003 Thomson Derwent. All rts. reserv.

015254997

Computer aided design system for improved design, has extraction mechanism, display, allowing mechanism and storage arrangement.

29/AZ, TI/5 (Item 5 from file: 350)
DIALOG(R) File 350:(c) 2003 Thomson Derwent. All rts. reserv.

014986102

File format translator writes set of features of source file into target file in target format using back-end lookup table

29/AZ, TI/6 (Item 6 from file: 350)
DIALOG(R) File 350:(c) 2003 Thomson Derwent. All rts. reserv.

014884680

Software product store keeping unit management method for computer system, involves providing information about authorized store keeping unit, based on matching of encrypted brands

29/AZ, TI/7 (Item 7 from file: 350)
DIALOG(R) File 350:(c) 2003 Thomson Derwent. All rts. reserv.

014802937

Compile system has subroutine call management unit which replaces object code with executable program, such that object code is loaded during program execution operation

29/AZ, TI/8 (Item 8 from file: 350)
DIALOG(R) File 350:(c) 2003 Thomson Derwent. All rts. reserv.

014053067

Method and device for reducing additional expenditure micro-code look-up operations improves the rate for converting *macro*-*commands* into corresponding micro-*commands* while *adding* coding to a micro-code memory device.

29/AZ, TI/9 (Item 9 from file: 350)

DIALOG(R) File 350:(c) 2003 Thomson Derwent. All rts. reserv.

014009405

Debug system of routine-work program for handheld terminals, performs symbolic debug by integrating debug function in interpreter and communicating with host computer and handheld terminal

29/AZ, TI/10 (Item 10 from file: 350)

DIALOG(R) File 350:(c) 2003 Thomson Derwent. All rts. reserv.

013242800

Method for modifying executable files including several compiled instructions by detecting whether instruction is patch instruction which transfers flow of execution to loaded associated converted instruction group

29/AZ, TI/11 (Item 11 from file: 350)

DIALOG(R) File 350:(c) 2003 Thomson Derwent. All rts. reserv.

013193070

Extensible macro language providing method for use in computer language processors, involves retrieving code associated with keywords representing new macro command, which is then executed

29/AZ, TI/12 (Item 12 from file: 350)

DIALOG(R) File 350:(c) 2003 Thomson Derwent. All rts. reserv.

012988072

Error pointer recording apparatus for floating point macro instruction in microprocessor

29/AZ, TI/13 (Item 13 from file: 350)

DIALOG(R) File 350:(c) 2003 Thomson Derwent. All rts. reserv.

012827535

Database accessing system for object oriented language processing

29/AZ, TI/14 (Item 14 from file: 350)

DIALOG(R) File 350:(c) 2003 Thomson Derwent. All rts. reserv.

012001684

Computerised data processing system - designates pointer and identifier and uses each data as starting point during processing when following a coupling path

29/AZ, TI/15 (Item 15 from file: 350)

DIALOG(R) File 350:(c) 2003 Thomson Derwent. All rts. reserv.

011651090

Relational data base management method with document search function - involves extracting first and second record identifier from document number and attribute value of search request signal based on which

document data from data base is searched

29/AZ, TI/16 (Item 16 from file: 350)
DIALOG(R) File 350: (c) 2003 Thomson Derwent. All rts. reserv.

011546961

Database updating system e.g. banking system - includes data processing part which produces macroinstruction containing positional information of accessed database for renewal process

29/AZ, TI/17 (Item 17 from file: 350)
DIALOG(R) File 350: (c) 2003 Thomson Derwent. All rts. reserv.

008548723

Multi-national message processing system - uses message processor logic, to combine file message data with data supplied by *executing* application

29/AZ, TI/18 (Item 18 from file: 350)
DIALOG(R) File 350: (c) 2003 Thomson Derwent. All rts. reserv.

007963574

Document recovery system from data base - has facility to combine key word combinations dependent upon user and frequency of use

29/AZ, TI/19 (Item 19 from file: 350)
DIALOG(R) File 350: (c) 2003 Thomson Derwent. All rts. reserv.

004376431

Mini-computer having address translation unit - uses sixteen or thirty-two bit logic addresses with macro-instruction decoder, dual port cache memory and bank control being provided

29/AZ, TI/20 (Item 20 from file: 347)
DIALOG(R) File 347: (c) 2003 JPO & JAPIO. All rts. reserv.

07419450

COMMAND STEAL SYSTEM IN MACRO CODE

29/AZ, TI/21 (Item 21 from file: 347)
DIALOG(R) File 347: (c) 2003 JPO & JAPIO. All rts. reserv.

06948137

DEVICE AND METHOD FOR ADAPTIVE DICTIONARY

29/AZ, TI/22 (Item 22 from file: 347)
DIALOG(R) File 347: (c) 2003 JPO & JAPIO. All rts. reserv.

05904334

SECURITY KEEPING SYSTEM

29/AZ, TI/23 (Item 23 from file: 347)
DIALOG(R) File 347: (c) 2003 JPO & JAPIO. All rts. reserv.

05890787

MULTI-FUNCTIONAL COPYING MACHINE

29/AZ, TI/24 (Item 24 from file: 347)
DIALOG(R) File 347:(c) 2003 JPO & JAPIO. All rts. reserv.
04360598
MULTIPROCESSOR

29/AZ, TI/25 (Item 25 from file: 347)
DIALOG(R) File 347:(c) 2003 JPO & JAPIO. All rts. reserv.
04189973
MICROPROCESSOR

29/AZ, TI/26 (Item 26 from file: 347)
DIALOG(R) File 347:(c) 2003 JPO & JAPIO. All rts. reserv.
03459821
UNDEFINED SYMBOL EXECUTING SYSTEM

29/AZ, TI/27 (Item 27 from file: 347)
DIALOG(R) File 347:(c) 2003 JPO & JAPIO. All rts. reserv.
03304701
METHOD AND DEVICE FOR DIGITAL CONTROL

29/AZ, TI/28 (Item 28 from file: 347)
DIALOG(R) File 347:(c) 2003 JPO & JAPIO. All rts. reserv.
03031626
PROGRAM SIZE REDUCING DEVICE AND METHOD

29/AZ, TI/29 (Item 29 from file: 347)
DIALOG(R) File 347:(c) 2003 JPO & JAPIO. All rts. reserv.
02972537
SYSTEM FOR TAKING OVER COMPILATION VARIABLE

29/AZ, TI/30 (Item 30 from file: 347)
DIALOG(R) File 347:(c) 2003 JPO & JAPIO. All rts. reserv.
02551033
SUBPROGRAM CALLING SYSTEM IN INTERPRETER

29/AZ, TI/31 (Item 31 from file: 347)
DIALOG(R) File 347:(c) 2003 JPO & JAPIO. All rts. reserv.
02021846
DATA PROCESSOR

29/AZ, TI/32 (Item 32 from file: 347)
DIALOG(R) File 347:(c) 2003 JPO & JAPIO. All rts. reserv.
01806036
PROGRAM COUNTER CONTROL SYSTEM

- S27

45 RD (un[REDACTED] items)

27/3,K/22 (Item 9 from file: 2)
DIALOG(R)File 2:INSPEC
(c) 2003 Institution of Electrical Engineers. All rts. reserv.

01815625 INSPEC Abstract Number: C82011976
Title: Programming aids-rapid evolution or slow revolution?
Author(s): McCartney, J.
Journal: Data Processing vol.23, no.9 p.20-3
Publication Date: Oct. 1981 Country of Publication: UK
CODEN: DPROAT ISSN: 0011-684X
Language: English
Subfile: C

...Abstract: evolution of systems software. Modular coding and online interactive development jockey for position with language *extensions* and *macro* *commands*, while data dictionary systems aim at removing file handling from the mainstream programming process.

27/3,K/23 (Item 10 from file: 2)
DIALOG(R)File 2:INSPEC
(c) 2003 Institution of Electrical Engineers. All rts. reserv.

01078772 INSPEC Abstract Number: C77018459
Title: KATE: a *macro*-processor for *extending* *command* languages
Author(s): Teskey, N.
Author Affiliation: Univ. of Kent, Canterbury, UK
Journal: Computer Journal vol.20, no.2 p.187-9
Publication Date: May 1977 Country of Publication: UK
CODEN: CMPJA6 ISSN: 0010-4620
Language: English
Subfile: C

Title: KATE: a *macro*-processor for *extending* *command* languages

27/3,K/25 (Item 12 from file: 2)
DIALOG(R)File 2:INSPEC
(c) 2003 Institution of Electrical Engineers. All rts. reserv.

00264447 INSPEC Abstract Number: C71012124
Title: Principles of realisation of a macro-generator
Author(s): Gros, J.
Journal: Revue Francaise d'Informatique et de Recherche Operationnelle
vol.4, no.B3 p.109-24
Publication Date: Dec. 1970 Country of Publication: France
CODEN: RFIOAY
Language: French
Subfile: C

Abstract: After introducing *macro* *instruction* *extensions* and language- independent *macro* processing concepts, a macro-generator is described. It provides all the features normally associated with...

27/3,K/37 (Item 1 from file: 434)
DIALOG(R)File 434:SciSearch(R) Cited Ref Sci
(c) 1998 Inst for Sci Info. All rts. reserv.

01550663 Genuine Article#: DJ461 No. References: 7
**Title: DISCUSSION AND CORRESPONDENCE KATE - *MACRO*-PROCESSOR FOR
EXTENDING *COMMAND* LANGUAGES**
Author(s): TESKEY N
Corporate Source: UNIV KENT,LEGAL RES COMP & COMMUN UNIT/CANTERBURY CT2
7NJ/KENT/ENGLAND/

Journal: COMPUTER JOURNAL, 1977, V20, N2, P187-189 (

Language: ENGLISH Document Type: NOTE

Title: DISCUSSION AND CORRESPONDENCE KATE - *MACRO*-PROCESSOR FOR
EXTENDING *COMMAND* LANGUAGES

27/AA,AN,TI/1 (Item 1 from file: 35)
DIALOG(R)File 35:(c) 2003 ProQuest Info&Learning. All rts. reserv.

01684367

A THREE-TIER DATABASE SYSTEM FOR JAVA REMOTE OBJECTS (INTERNET)

27/AA,AN,TI/2 (Item 2 from file: 35)
DIALOG(R)File 35:(c) 2003 ProQuest Info&Learning. All rts. reserv.

01611132

CRACK TIP MECHANICS IN PERIODICALLY LAYERED COMPOSITES (FRACTURE, ELASTIC PLASTIC RESPONSE, STRESS)

27/AA,AN,TI/3 (Item 3 from file: 35)
DIALOG(R)File 35:(c) 2003 ProQuest Info&Learning. All rts. reserv.

01303025

DEVELOPMENT OF EAGLE MACRO ELEMENTS FOR CONSTRUCTION OF GENERIC GRIDS

27/AA,AN,TI/4 (Item 4 from file: 35)
DIALOG(R)File 35:(c) 2003 ProQuest Info&Learning. All rts. reserv.

1032315

MULTI-LEVEL ITERATIVE TECHNIQUES

27/AA,AN,TI/5 (Item 1 from file: 8)
DIALOG(R)File 8:(c) 2003 Elsevier Eng. Info. Inc. All rts. reserv.

05249406

E.I. No: EIP99034603845

Title: Integrated software environment for the design and real-time implementation of control systems

27/AA,AN,TI/6 (Item 2 from file: 8)
DIALOG(R)File 8:(c) 2003 Elsevier Eng. Info. Inc. All rts. reserv.

04562413

E.I. No: EIP96113421086

Title: Software architecture approach for designing CASE systems

27/AA,AN,TI/7 (Item 3 from file: 8)
DIALOG(R)File 8:(c) 2003 Elsevier Eng. Info. Inc. All rts. reserv.

04306491

E.I. No: EIP95122954527

Title: Implementation and performance of composite fast FIR filtering algorithms

27/AA,AN,TI/8 (Item 4 from file: 8)
DIALOG(R)File 8:(c) 2003 Elsevier Eng. Info. Inc. All rts. reserv.

03038205

E.I. Monthly No: EIM9103-012121

Title: Theory and application of the newly developed high-efficient flux for treating aluminium or aluminium alloys.

27/AA,AN,TI/9 (Item 5 from file: 8)

DIALOG(R)File 8:(c) 3 Elsevier Eng. Info. Inc. All rts. reserv.

02109648

E.I. Monthly No: EIM8608-052238

Title: INTERACTIVE SOFTWARE PACKAGE FOR COMPUTER AIDED DESIGN OF CONTROL SYSTEMS.

27/AA,AN,TI/10 (Item 6 from file: 8)

DIALOG(R)File 8:(c) 2003 Elsevier Eng. Info. Inc. All rts. reserv.

02021542

E.I. Monthly No: EI8609081568

Title: CONVERSION OF A TOKEN THREADED LANGUAGE TO AN ADDRESS THREADED LANGUAGE.

27/AA,AN,TI/11 (Item 7 from file: 8)

DIALOG(R)File 8:(c) 2003 Elsevier Eng. Info. Inc. All rts. reserv.

01402494

E.I. Monthly No: EI8311092374

Title: PROGRAMMABLE COMPILER.

27/AA,AN,TI/12 (Item 8 from file: 8)

DIALOG(R)File 8:(c) 2003 Elsevier Eng. Info. Inc. All rts. reserv.

01359648

E.I. Monthly No: EI8306041152

Title: STOIC, AN INTERACTIVE PROGRAMMING SYSTEM FOR DEDICATED COMPUTING.

27/AA,AN,TI/13 (Item 1 from file: 202)

DIALOG(R)File 202:(c) 2003 EBSCO Publishing. All rts. reserv.

0401822

Abacus - ab atomic energy computerized user-oriented services: the mechanization of bibliographic list production.

27/AA,AN,TI/14 (Item 1 from file: 2)

DIALOG(R)File 2:(c) 2003 Institution of Electrical Engineers. All rts. reserv.

Title: Hierarchical execution to speed up pipeline interlock in mainframe computers

27/AA,AN,TI/15 (Item 2 from file: 2)

DIALOG(R)File 2:(c) 2003 Institution of Electrical Engineers. All rts. reserv.

Title: Exploiting the induced order on type-labeled graphs for fast knowledge retrieval

27/AA,AN,TI/16 (Item 3 from file: 2)

DIALOG(R)File 2:(c) 2003 Institution of Electrical Engineers. All rts. reserv.

Title: The data-parallel Ada run-time system, simulation and empirical results

27/AA,AN,TI/17 (Item 4 from file: 2)
DIALOG(R)File 2:(c) 2003 Institution of Electrical Engineers. All rts.
reserv.

Title: Spreadsheet approach to partial differential equations. I. Elliptic equation

27/AA,AN,TI/18 (Item 5 from file: 2)
DIALOG(R)File 2:(c) 2003 Institution of Electrical Engineers. All rts.
reserv.

Title: OODDM: An object-oriented database design model

27/AA,AN,TI/19 (Item 6 from file: 2)
DIALOG(R)File 2:(c) 2003 Institution of Electrical Engineers. All rts.
reserv.

Title: A power MOSFET macro-model for circuit simulation

27/AA,AN,TI/20 (Item 7 from file: 2)
DIALOG(R)File 2:(c) 2003 Institution of Electrical Engineers. All rts.
reserv.

Title: Staircase telescope arrays for local beam compression in one dimension

27/AA,AN,TI/21 (Item 8 from file: 2)
DIALOG(R)File 2:(c) 2003 Institution of Electrical Engineers. All rts.
reserv.

Title: Text editing with EMACS

27/AA,AN,TI/22 (Item 9 from file: 2)
DIALOG(R)File 2:(c) 2003 Institution of Electrical Engineers. All rts.
reserv.

Title: Programming aids-rapid evolution or slow revolution?

27/AA,AN,TI/23 (Item 10 from file: 2)
DIALOG(R)File 2:(c) 2003 Institution of Electrical Engineers. All rts.
reserv.

Title: KATE: a *macro*-processor for *extending* *command* languages

27/AA,AN,TI/24 (Item 11 from file: 2)
DIALOG(R)File 2:(c) 2003 Institution of Electrical Engineers. All rts.
reserv.

Title: Heuristic techniques in computer-aided circuit analysis

27/AA,AN,TI/25 (Item 12 from file: 2)
DIALOG(R)File 2:(c) 2003 Institution of Electrical Engineers. All rts.
reserv.

Title: Principles of realisation of a macro-generator

27/AA,AN,TI/26 (Item 1 from file: 94)
DIALOG(R)File 94:(c)2003 Japan Science and Tech Corp(JST). All rts.
reserv.

03779780 JICST ACCESSION NUMBER: 98A0881814
A performance evaluation of Run-time Restructuring architecture testbed
"Ocha-Pro".

27/AA,AN,TI/27 (Item 2 from file: 94)
DIALOG(R)File 94:(c)2003 Japan Science and Tech Corp(JST). All rts.
reserv.

03600324 JICST ACCESSION NUMBER: 98A0620356
A detailed discussion on the extension of the set oriented language SOL by
introducing indefinite type and labeled map notations.

27/AA,AN,TI/28 (Item 3 from file: 94)
DIALOG(R)File 94:(c)2003 Japan Science and Tech Corp(JST). All rts.
reserv.

03542726 JICST ACCESSION NUMBER: 98A0323368
The design and implementation of high performance and portable Java Virtual
Machine.

27/AA,AN,TI/29 (Item 1 from file: 6)
DIALOG(R)File 6:(c) 2003 NTIS, Intl Cpyrght All Rights Res. All rts.
reserv.

NTIS Accession Number: PB98-126949/XAB
NGIS SIM Specification
(Draft rept)

27/AA,AN,TI/30 (Item 2 from file: 6)
DIALOG(R)File 6:(c) 2003 NTIS, Intl Cpyrght All Rights Res. All rts.
reserv.

NTIS Accession Number: DE96010390
Seasonal trends in growth and biomass accumulation of selected nutrients
and metals in six species of emergent aquatic macrophytes

27/AA,AN,TI/31 (Item 3 from file: 6)
DIALOG(R)File 6:(c) 2003 NTIS, Intl Cpyrght All Rights Res. All rts.
reserv.

NTIS Accession Number: AD-A221 462/5
Real-Time Measurement of Mental Workload Using Psychophysiological
Measures
(Interim rept. Jan-Dec 89)

27/AA,AN,TI/32 (Item 4 from file: 6)
DIALOG(R)File 6:(c) 2003 NTIS, Intl Cpyrght All Rights Res. All rts.
reserv.

NTIS Accession Number: AD-A219 923/0
Sharing Memory Robustly in Message-Passing Systems
(Technical rept)

27/AA,AN,TI/33 (Item 5 from file: 6)

DIALOG(R)File 6:(c) 3 NTIS, Intl Cpyrght All Rights Res. All rts.
reserv.

NTIS Accession Number: AD-A217 488/6
Procedure for Recording Multiple-Exposure Holograms with Equal
Diffraction Efficiency in Photorefractive Media

27/AA,AN,TI/34 (Item 6 from file: 6)
DIALOG(R)File 6:(c) 2003 NTIS, Intl Cpyrght All Rights Res. All rts.
reserv.

NTIS Accession Number: AD-A179 344/7
Modeling and Simulation of the WFTA (Winograd Fourier Transform
Algorithm) 16 PFA (Prime Factor Algorithm) Processor Using the VHSIC (Very
High Speed Integrated Circuit) Hardware Description Language. Volume 1
(Master's thesis)

27/AA,AN,TI/35 (Item 7 from file: 6)
DIALOG(R)File 6:(c) 2003 NTIS, Intl Cpyrght All Rights Res. All rts.
reserv.

NTIS Accession Number: PB86-228616
Release Notes for STAT2 Version 1.7: An Addendum to NBS (National Bureau
of Standards) Special Publication 400-75

27/AA,AN,TI/36 (Item 8 from file: 6)
DIALOG(R)File 6:(c) 2003 NTIS, Intl Cpyrght All Rights Res. All rts.
reserv.

NTIS Accession Number: AD-474 019/7/XAB
Calculaid: An on-Line System for Algebraic Computation and Analysis
(Master's thesis)

27/AA,AN,TI/37 (Item 1 from file: 434)
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01550663
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TITLE: Java: Internet Toy or Enterprise Tool?

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S1	10245403	EXTEN? OR UNDEFINED OR (NON OR "NOT" OR NO OR WITHOUT OR WITH() OUT OR OMIT? ? OR OMITT???) (3W) (DEFIN? OR DESIGNAT??? OR STIPULAT??? OR SET OR DETERMIN? OR ESTABLISH?) OR ADD??? OR A- DDITIONAL
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S10	6899	S5(S) (S6 OR S7)
S11	0	S8(S) S9(S) S10
S12	0	S8 AND S9 AND S10
S13	1	S8(S) (S9 OR S10)
S14	289	S2 AND (S9 OR S10)
S15	3752	S1(5N) S2
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Keywords advanced version 1.0. (keyboard enhancement package) (Software Review) (evaluation)

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01246226 SUPPLIER NUMBER: 06811867
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New compiler technology boosts Microsoft QuickBASIC 4.0 productivity.

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January through June, 1987. (Lotus mid-year index)

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Upward compatibility: some observations for people who upgrade from 1-2-3 Releases 1A or 2. (1-2-3 Macros) (column)

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